

Lesson 10

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Make

Sources and Solutions

- we publish all code written in this course at https://github.com/jkrbs/c_lessons
- we will publish example solutions of the tasks on same site
- send us questions or your solutions to c-lessons@deutschland.gmbh

Make

you have seen the gcc commands. they get complicated for big projects with multiple libraries.

for projects with multiple files we usually use build systems

- cmake
- gnu/make
- autotools
- ...

Makefile

```
CC := gcc
CFLAGS := -Wall
DFLAGS := $(CFLAGS) -g
RFLAGS := $(CFLAGS) -O2
TARGET := target
NAME := our_binary_name
SRC := $(shell find src/ -iname '*.c')
.PHONY release clean debug #define all targets
release:
$(CC) $(RFLAGS) -o $(target)/$(NAME) $(SRC)
debug:
$(CC) $(DFLAGS) -o $(target)/$(NAME) $(SRC)
clean:
@rm $(TARGET)/*
```

write your makefile in a file called “Makefile” in the root directory of your project

```
$ make # in the root directory of your project
```

```
$ make clean
```

```
$ make debug
```